

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L13	1	(charge adj pump) and (loop adj filter) and (bias adj generator) and (((duty adj cycle) or duty-cycle) adj distortion)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/10 15:31
L17	22	fayneh-eyal.IN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/10 16:28
L23	120	714/707.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/10 16:48
L24	140	714/709.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/10 16:50
L25	348	714/55.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/10 16:51
L28	277	327/293.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/10 16:55
L29	2511	375/376.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/10 16:55
L30	905	327/158.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/10 16:55
L31	744	327/175.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/10 16:55

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L32	277	327/299.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/10 16:55
L33	1	(charge adj pump) and (loop adj filter) and (bias adj generator) and (((duty adj cycle) or duty-cycle) with correct\$3 with distort\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/10 18:21
L35	18	(knoll-ernest.IN.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/10 18:26
L36	352	714/700.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/10 18:26

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L34	1	(charge adj pump) and (loop adj filter) and (bias adj generator) and (((duty adj cycle) or duty-cycle) with correct\$3 with distort\$3)	US-PGPUB; USPAT	OR	OFF	2006/06/10 18:21



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#1 ((duty cycle<in>metadata) <and> (charge pump<in>metadata))<and> (bias generator<in>metadata)

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Relevance scale ☐ ☐ ☐ ☐ ☐1 A resonant clock generator for single-phase adiabatic systems

Conrad H. Ziesler, Suhwan Kim, Marios Papaefthymiou

August 2001 **Proceedings of the 2001 international symposium on Low power electronics and design**

Publisher: ACM Press

Full text available: pdf(212.10 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)
Keywords: CMOS, SCAL, SCAL-D, TSEL, VLSI, adiabatic logic, clock generator, dynamic circuitry, low energy, resonant, single phase

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